

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph on page 51, lines 1-13, as follows:

A portion of system memory **4420** as shown is occupied by the operating system (OS **4425**), which includes kernel **4430** and loaded SPU device module **4435**. SPU device module **4435** is loaded in order to facilitate device-like access to the SPUs by executing applications, such as application **4440**. When an application such as application **4430** requests to send code and data to one of the SPUs, the kernel, using SPU device module **4435**, forwards the request to the appropriate SPU using the appropriate SPU instructions. In a Unix environment, SPU **4450** may be accessed using the device access path, "/dev/spu1", SPU **4470** may be accessed using the device access path, "/dev/spu2", SPU **4490** may be accessed using the device access path, "/dev/spuN", etc.

Please amend the paragraph on page 51, lines 14-28, as follows:

Figure 45A is a table illustrating a device configuration file that contains device access paths and corresponding devices. In one embodiment, Table **4510** contains a first column with a list of device access paths and a second column with a list of the actual device that corresponds to each of the device paths. In a Unix environment, for example, the device access paths may be of the form, "/dev/xxx" where "xxx" is the name assigned to a device. "/dev/spu1", for example, may correspond to SPU 1, "/dev/spu2" may correspond to SPU 2, etc. In addition, the configuration file may contain device access paths for other devices attached to the computer system such as disk drives, printers, etc. The device configuration file may be stored on disk, in conventional RAM, in nonvolatile RAM, ROM, etc.

Please amend the paragraph on page 52, lines 9-18, as follows:

Figure 46 is a flowchart illustrating a method for setting up device-like access to a system's multiple processors. Processing begins at **4600** whereupon, at step **4610**, the SPU device module is received and then loaded into operating system kernel **4620** in system memory **[[4620]] 4618** of the computer system at step **4615**. The device module provides the kernel with the ability to communicate with the SPUs by providing the interface parameters between executing applications requesting access to SPUs and the SPUs.

Please amend the paragraph on page 53, lines 22-27, as follows:

At step **4715**, the operating system kernel loads the application **4720** into system memory **[[4720]] 4718** and initiates execution of the application. In one embodiment, the application executes on the main processor of the computer system. In another embodiment, the application may execute on one of the computer system's other processors.

Please amend the paragraph on page 54, lines 18-21, as follows:

If an application has issued a request for an SPU, decision **4730** branches to "yes" branch **[[4740]] 4735** whereupon, at step **4745**, the application's SPU instructions and data are placed for the SPU in system memory.

Please amend the paragraph on page 56, lines 19-28, as follows:

If instructions have been placed in the SPU's mailbox, decision **4915** branches to "yes" branch **[[4925]] 4920** whereupon, at step **4940**, the SPU accesses the SPU's mailbox and reads the instructions (or the pointer to the instructions placed in system memory). At step **4945**, the SPU, using DMA, transfers the instructions (prepared using

the SPU device module) to the SPU's local store, and at step **4950**, the SPU, again using DMA, transfers the application data to be processed from system memory **4930** to the SPU's local store as well.

Please amend the paragraph on page 57, lines 1-9, as follows:

At step ~~[[4960]]~~ **4955**, the SPU processes the data placed there by the application. In one embodiment, as the SPU processes the data, the SPU places the data back in the SPU's local store. At step **4960**, the SPU transfers the processed data from the SPU's local store into the output buffer reserved for the SPU's processed data in system memory **4930**. The SPU may transfer the data periodically as the SPU processes the data or the SPU may transfer the data after the SPU has finished processing all the data.